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UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) William J. Taylor, Jr. et al. GROUP ART UNIT: 2823
APPLN. NO.: 10/670,634 EXAMINER: William M. Brewster
FILED: September 25, 2003 CONFIRMATION NO. 6860
TITLE: SEMICONDUCTOR PROCESS FOR DISPOSABLE SIDEWALL
SPACERS AND STRUCTURE

Certificate of Transmission under 37 C.F.R. 1.8

I hereby certify that this correspondence is being
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Signature

Pat Thomas
Printed Name of Person Signing Certificate

ISSUE OF PATENT TO ASSIGNEE PURSUANT TO 37 CFR 3.81

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Payment of the Issue Fee of this application is concurrently made with the request
that the application issue in the name of Freescale Semiconductor, Inc. An assignment of all
rights, title and interest in the instant application from Motorola, Inc. to Freescale
Semiconductor, Inc. has been previously filed for recordation in accordance with 37 CFR
3.11, but it is not believed to have been recorded by the USPTO as of yet.

Respectfully submitted,

SEND CORRESPONDENCE TO:

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